1. Design a common source single stage MOSFET amplifier circuit to have a small-signal gain with a magnitude of 5. Bias the circuit so that the dc drain current is 1 mA. The small-signal input impedance is specified to be 200 kΩ and the small signal output impedance is 7.5 kΩ. The load resistor is 75 kΩ. The dc power supply voltages are $V^+ = +15$ V and $V^- = -15$ V. The parameters of the transistor are: $K$, $V_{TO}$, $\lambda$, $C_{GDO} = 2.5 \, \text{nF/m}$, and $C_{GSO} = 2.5 \, \text{nF/m}$. Pick $C_1 = C_2 = 0.22 \, \mu\text{F}$, and $C_3 = 10 \, \mu\text{F}$. For $K$ and $V_{TO}$ use the values measured in lab from the data taken from a curve tracer.

   For the SPICE analysis use a DC analysis to determine the bias. Use an AC analysis to plot the gain versus the frequency. Choose the lower frequency as 1 Hz and the upper frequency 10 GHz. Mark the midband gain and the $-3 \, \text{dB}$ frequencies. The SPICE parameters are $K$ ($2K$), $V_{TO}$ ($V_{TO}$), $\lambda$ ($\lambda$), $C_{GDO}$ ($C_{GDO}$), and $C_{GSO}$ ($C_{GSO}$). If the version of SPICE used requires the width ($W$) and length ($L$) of the channel use 10 μm for each. Perform a transient analysis to determine the upper and lower clipping levels.